Rev. A, June 2001



FJC1963

Audio Power Amplifier Applications

- Complement to FJC1308
- High Collector Current
- Low Collector-Emitter Saturation Voltage



1. Base 2. Collector 3. Emitter

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

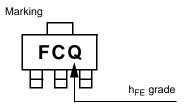
Symbol	Parameter	Value	Units
V_{CBO}	Collector-Base Voltage	50	V
V_{CEO}	Collector-Emitter Voltage	30	V
V_{EBO}	Emitter-Base Voltage	6	V
I _C	Collector Current (DC)	3	Α
P _C	Power Dissipation(T _C =25°C)	0.5	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{C}=50\mu A, I_{E}=0$	50			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C =1mA, I _B =0	30			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_{E}=50\mu A, I_{C}=0$	6			V
I _{CEO}	Collector Cut-off Current	V_{CE} =40V, V_{B} =0			0.5	μΑ
I _{EBO}	Emitter Cut-off Current	V_{EB} =5V, I_{C} =0			0.5	μΑ
h _{FE}	DC Current Gain	V_{CE} =2V, I_{C} =0.5A	120		560	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =1.5, I _B =0.15A			0.45	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C =1.5, I _B =0.15A			1.2	V

h_{FE} Classification

Classification	Q	R	S
h _{FE}	120 ~ 270	180 ~ 390	280 ~ 560



©2001 Fairchild Semiconductor Corporation

Typical Characteristics

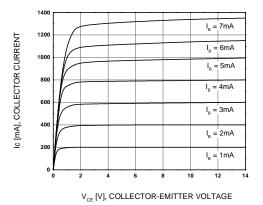


Figure 1. Static Characteristic

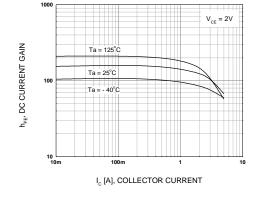


Figure 2. DC current Gain

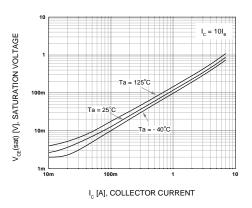


Figure 3. Collector-Emitter Saturation Voltage

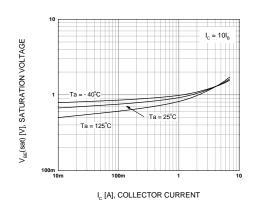


Figure 4. Base-Emitter Saturation Voltage

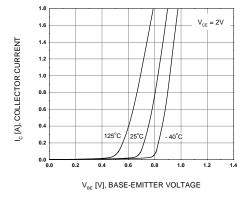


Figure 5. Base-Emitter On Voltage

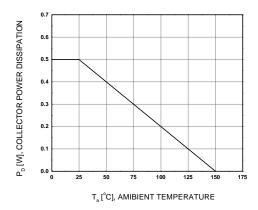
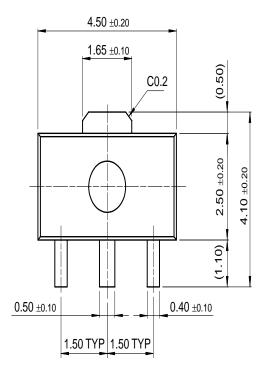


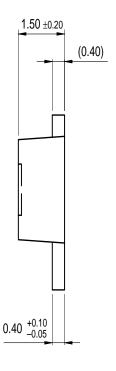
Figure 6. Power Derating

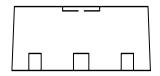
©2001 Fairchild Semiconductor Corporation Rev. A, June 2001

Package Demensions

SOT-89







TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™	FAST [®]	OPTOPLANAR™	STAR*POWER™
Bottomless™	FASTr™	PACMAN™	Stealth™
CoolFET™	FRFET™	POP^TM	SuperSOT™-3
$CROSSVOLT^{TM}$	GlobalOptoisolator™	Power247™	SuperSOT™-6
DenseTrench™	GTO™	PowerTrench [®]	SuperSOT™-8
DOME™	HiSeC™	QFET™	SyncFET™
EcoSPARK™	ISOPLANAR™	QS™	TruTranslation™
E ² CMOS™	LittleFET™	QT Optoelectronics™	TinyLogic™
EnSigna™	MicroFET™	Quiet Series™	UHC™
FACT™	MICROWIRE™	SLIENT SWITCHER®	UltraFET [®]
FACT Quiet Series™	OPTOLOGIC™	SMART START™	VCX™

STAR*POWER is used under license

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

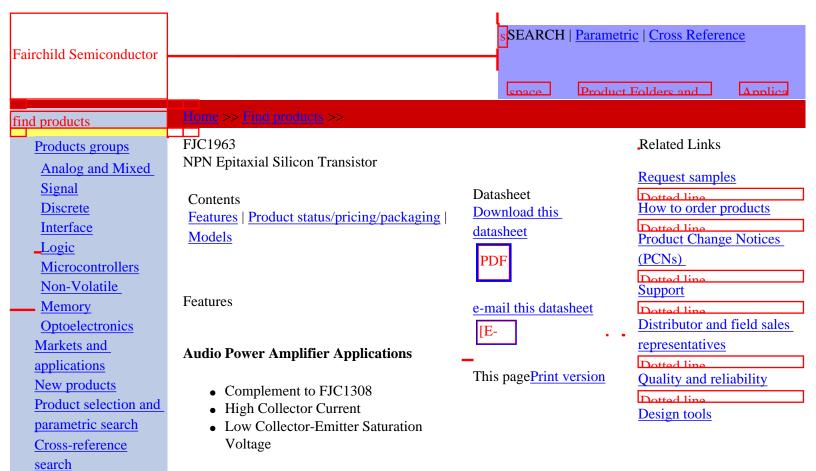
- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

©2001 Fairchild Semiconductor Corporation Rev. H3



technical information

buy products

technical support

my Fairchild

company

back to top

Product status/pricing/packaging

Product	Product status	Pricing*	Package type	Leads	Packing method
FJC1963STF	Full Production	\$0.203	<u>SOT-89</u>	3	TAPE REEL
FJC1963QTF	Full Production	\$0.203	<u>SOT-89</u>	3	TAPE REEL
FJC1963RTF	Full Production	\$0.203	<u>SOT-89</u>	3	TAPE REEL

^{* 1,000} piece Budgetary Pricing

back to top

Models

Package & leads	Condition Temperature range		Software version	Revision date
PSPICE				
SOT-89-3	Electrical	-25°C to 125°C	9.2	Nov 27, 2001

back to top

<u>Home</u> | <u>Find products</u> | <u>Technical information</u> | <u>Buy products</u> | <u>Support</u> | <u>Company</u> | <u>Contact us</u> | <u>Site index</u> | <u>Privacy policy</u>

© Copyright 2002 Fairchild Semiconductor